

CALL FOR PAPERS

PRIME has been established over the recent years as an important conference where PhD students and post-docs with less than one year post-PhD experience can present their research results and network with experts from industry, academia and research. PRIME 2017 will feature conference program reflecting the wide spectrum of research topics in Microelectronics and Electronics, building bridges between various research fields. In addition to the technical sessions, opportunities for the conference attendees will be the keynote talks, workshops and social events. PRIME 2017 is seeking original papers in the areas given below. PRIME 2017 is technically co-sponsored by IEEE and IEEE CAS. All accepted papers will be published in IEEE Xplore. A selection of presented papers will be considered for a special issue of Integration, the VLSI Journal, Elsevier.



Areas of Interest

- Micro/Nanoelectronics
- Semiconductors
- Analog/Digital Signal Processing
- Computer Aided Design
- Analog/Digital/Mixed/RF IC Design
- Integrated Power ICs
- Sensors/Systems and MEMS
- Semiconductor Memories
- RF, Microwave and mm-wave Circuits
- VLSI and SoC Applications
- Visual Signal Processing
- Energy Harvesting
- Automotive
- Flexible Electronics
- Technical Trends & Challenges

Best Paper Awards

Top 10% papers: Gold Leaf Certificate
10-20%: Silver Leaf Certificate
20-30%: Bronze Leaf Certificate

Important Dates

Submission Deadline: 17^{th} Feb 2017Author Notification: 1^{st} Apr 2017Camera Ready Paper Submission: 15^{th} Apr 2017Early Registration Deadline: 15^{th} Apr 2017

Venue

PRIME 2017 will be held at Naxos Beach Hotel Congress Centre, Giardini Naxos, Italy. PRIME 2017 will be colocated with SMACD 2017 (SMACD2017.unisa.it)

Organizing Committee

General Chair:

Salvatore Pennisi, University of Catania, Italy

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